EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	1808	(test adj bench)	US-PGPUB; USPAT	OR	ON	2006/06/06 08:15
L7	1084	L6 and internal	US-PGPUB; USPAT	OR	ON	2006/06/06 08:15
L8	79	L7 and (timing adj constraint\$1)	US-PGPUB; USPAT	OR	ON	2006/06/06 08:15
L9	78	L8 and simulat\$3	US-PGPUB; USPAT	OR	ON	2006/06/06 08:15
L10	73	L9 and verificat\$3	US-PGPUB; USPAT	OR	ON	2006/06/06 08:15
L11	43	l10 and event	US-PGPUB; USPAT	OR	ON	2006/06/06 08:15
L14	404	703/15.ccls.	US-PGPUB; USPAT	OR	ON	2006/06/06 10:22
S1	932	702/117,123.ccls.	US-PGPUB; USPAT	OR	ON	2006/06/01 14:14
S2	12	S1 and (test adj bench)	US-PGPUB; USPAT	OR	ON	2006/06/01 16:53
S3	404	703/15.ccls.	US-PGPUB; USPAT	OR	ON	2006/06/01 14:19
S4	30	S3 and (test near bench)	US-PGPUB; USPAT	OR	ON	2006/06/01 14:19
S5	23	S4 and HDL	US-PGPUB; USPAT	OR	ON	2006/06/01 15:31
S6	2	("5903475" "5923867").PN.	US-PGPUB; USPAT	OR	ON	2006/06/01 15:35
S7	3	blasi-gianluca\$.in.	US-PGPUB; USPAT	OR	ON	2006/06/01 15:38
S8	5288	stmicroelectronics\$.as.	US-PGPUB; USPAT	OR	ON	2006/06/01 15:38
S9	8	S8 and (test adj bench)	US-PGPUB; USPAT	OR	ON	2006/06/01 16:30
S10	404	703/15.ccls.	US-PGPUB; USPAT	OR	ON	2006/06/01 16:33
S11	30	S10 and (test adj bench)	US-PGPUB; USPAT	OR	ON	2006/06/01 16:33
S12	23	S11 and ((HDL) or (hardware adj description adj language))	US-PGPUB; USPAT	OR	ON	2006/06/01 16:34
S13	8	S2 and (HDL or (hardware adj description adj language))	US-PGPUB; USPAT	OR	ON	2006/06/01 17:04

6/6/2006 10:28:06 AM C:\Documents and Settings\mhogan\My Documents\EAST\Workspaces\10603055.wsp Page 1

EAST Search History

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S14	2071	(test adj bench)	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:05
S15	365	S14 and (HDL or (hardware adj description adj language))	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:06
S16	1	S15 and (self near check)	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:06
S17	295	S15 and (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:07
S18	245	S17 and model\$4	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:07
S19	228	(test adj bench) near (generat\$3 or creat\$3 or compile\$2 or develop\$2)	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:08
S20	135	S19 and (HDL or (hardware adj description adj language))	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:09
S21	83	S20 and configuration	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:09
S22	73	S21 and behavior\$2	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:09
S23	72	S22 and memory	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:10
S24	13	S23 and proprietary	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2006/06/01 17:10
S25	2	("5903475" "5923867").PN.	US-PGPUB; USPAT	OR	ON	2006/06/05 11:01
S26	4	(US-20020059054-\$).did. or (US-6513143-\$ or US-6975976-\$ or US-6904397-\$).did.	US-PGPUB; USPAT	OR	ON	2006/06/05 17:17

EAST Search History

S27	2	S26 and timing	US-PGPUB; USPAT	OR	ON	2006/06/05 17:17
S28	2	S26 and internal	US-PGPUB; USPAT	OR	ON	2006/06/05 17:17
S29	1	S26 and (timing and internal)	US-PGPUB; USPAT	OR	ON	2006/06/05 17:19
S30	1804	(test adj bench)	US-PGPUB; USPAT	OR	ON	2006/06/05 17:19
S31	1	S30 and (internal near event\$1)	US-PGPUB; USPAT	OR	ON	2006/06/05 17:20
S32	1082	S30 and internal	US-PGPUB; USPAT	OR	ON	2006/06/05 17:20
S33	79	S32 and (timing adj constraint\$1)	US-PGPUB; USPAT	OR	ON	2006/06/05 17:20
S34	78	S33 and simulat\$3	US-PGPUB; USPAT	OR	ON	2006/06/05 17:21
S35	73	S34 and verificat\$3	US-PGPUB; USPAT	OR	ON	2006/06/05 17:21

6/6/2006 10:28:06 AM C:\Documents and Settings\mhogan\My Documents\EAST\Workspaces\10603055.wsp Page 3